

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD**
M.E. (E.C.E.) I-Semester Regular & PTPG and III-Semester PTPG (Main) Examination
January 2020

**Examination Centre:
Dept. of ECE
UCE (A), O.U.**

REVISED EXAMINATION TIME TABLE

Time : 2.00 PM TO 5.00 PM

Date and Day	Digital Systems			Systems & Signal Processing	Microwave & Radar Engineering		Embedded Systems and VLSI Design
	Regular I-Semester	PTPG I-Semester	PTPG III-Semester	Regular I-Semester	Regular I-Semester	PTPG III-Semester	Regular I-Semester
03-01-2020 FRIDAY	Advanced Computer Architecture	=====	Field Programmable Gate Arrays	Advanced Digital Signal Processing	=====	=====	Analog and Digital CMOS VLSI Design
06-01-2020 MONDAY	Microcontroller Architecture	Microcontroller Architecture	=====	=====	Microwave Measurements	Microwave Measurements	Software Defined Radio / VLSI Physical Design
08-01-2020 WEDNESDAY	English for Research Paper Writing	=====	Low Power VLSI Design	English for Research Paper Writing	English for Research Paper Writing	=====	English for Research Paper Writing
10-01-2020 FRIDAY	Wireless & Mobile Communications	=====	=====	Computer Vision	Microwave Antennas	Radar Signal Processing	Satellite Navigation Systems
20-01-2020 MONDAY	VLSI Design and Technology	VLSI Design and Technology	Advanced Computer Organization	Wireless Sensor Networks	Advanced Electromagnetic Engineering	=====	CPLD and FPGA Architecture
22-01-2020 WEDNESDAY	Internet of Things	=====	=====	Digital Image and Video Processing / Image & Video Processing	Satellite Radio Navigation	Wireless Communications and Networks	Microcontroller and Programmable Digital Signal Processing
24-01-2020 FRIDAY	Engineering Research Methodology in ECE (NS) Engineering Research Methodology (OS)						

Note: This Time-Table is also applicable for Pre-Ph.D. students


DIRECTOR OF EVALUATION
EXAMINATION CELL